

AMENDMENTS

None.

REMARKS

The Examiner rejected all the claims (1-16) under 35 USC § 103(a) as being unpatentable over Bell et al. (EP 0365445) in view of Parks ([US] 5471225). Regarding Bell, the Examiner stated in part that:

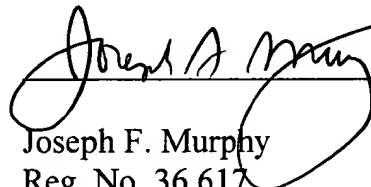
“ . . . Bell discloses . . . [a] memory cell compris[ing] a bitline input, power input, a select input, a current driver having an output current level proportional to a bit position of a column data line bit coupled to a bitline input of a memory element The output of the current driver has a relationship of [] [sic] where I_{out} is the output current, I_{base} is the interval current unit, and n is a bit position (see Fig. 2 and column 3, line 53 to column 4, line 37).”

Applicant respectfully disagrees with the Examiner's above statement. Bell does not disclose “. . . a current driver having an output current level proportional to a bit position of a column data line bit coupled to a bitline input of a memory element. . .” Moreover, Bell does not disclose certain currently claimed current relationships, i.e. Bell does not disclose either the claimed “. . . relationship: $I_{out}=I_{base}*2^n$ Where: I_{out} is output current of the current driver, I_{base} is the interval current unit for the grayscale step, and n is

the bit position of the output bit that is provided to the current driver from the memory element” (Claim 2 of the present application) nor does Bell disclose the claimed “. . . relationship: $I_{out} = I_{base} \cdot 2^n$ Where: I_{out} is the predetermined current of the current driver, I_{base} is the interval current unit for the grayscale step, and n is the bit position of the control signal output that is provided to the current driver from the associated memory element.” (Claim 11 of the present application).

In light of the foregoing arguments, Applicant respectfully requests that the Examiner’s objections and rejections be reconsidered and withdrawn, and that all claims be allowed.

Respectfully submitted,



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